


Name of Teaching Staff	:	Prof. V. Venkataramanan	
Designation	:	Assistant Professor	
Department	:	Electronics & Telecommunication Engineering	
Date of Joining the Institution	:	4.1.2016	
Email ID	:	venkataramanan.v@djsce.ac.in	
Office Contact	:	022-42335000 Ext: 1165	
Google Scholar Link	:	https://scholar.google.com/citations?hl=en&authuser=4&user=C39U3qkAAAAJ	
ResearchGate Link:		https://www.researchgate.net/profile/Venkataramanan-v	
ORCID		https://orcid.org/0000-0001-7270-3221	
Publons ID		https://publons.com/researcher/3993994/v-venkataramanan/ Researcher ID: ABE-8512-2020	
Qualifications with Class / Grade	:	<ol style="list-style-type: none"> 1. Completed Ph.D. in Electronics & communication Engineering from Sathyabama Institute of Science and Technology on Topic “Performance analysis of LTE Physical layer and its Implementation Using Reconfigurable Architecture” in August 2021. 2. M.Tech. – VLSI Design from SRM University in May 2010, 1st class with 7.94 CGPA. 3. M.Sc. (Electronics & Communication) 5 Year Integrated PG Degree program from Annamalai University in June 2008, 1st class with distinction 9.43 CGPA. (Mumbai University Equivalence is obtained) 	
Total Experience in Years	:	<p>Teaching: 10 years and 11 Months</p> <ol style="list-style-type: none"> 1. Assistant Professor, D.J. Sanghvi College of Engineering from 4.1.2016 to till date. 2. Assistant Professor, Arunai College of Engineering from 16.12.2010 to 31.10.2015. 	
Papers Published in Journal/Conference:	:	<p>International Journals (SCI) - 3</p> <ol style="list-style-type: none"> 1. S. Lakshmi, V. A. Kanetkar, and V. Venkataramanan, “Design and implementation of LTE physical layer on FPGA,” International Journal of Computer Applications in Technology, vol. 61, no. 1/2, p. 127, 2019. 2. V. Venkataramanan and S. Lakshmi, “Hardware co-simulation of LTE physical layer for mobile network applications,” Future Generation Computer Systems, vol. 99, pp. 124–133, Oct. 2019. 3. V. Venkataramanan and S. Lakshmi, “Performance analysis of LTE physical layer using hardware co-simulation techniques and implementation on FPGA for communication systems,” International Journal of Communication Systems, p. e4125, Aug. 2019. 	

International Journals (Scopus) - 5

1. V.Radhamani, **V. Venkataramanan**, S. Diwakaran, Muthukumar Subramanian, Arun Sekar Rajasekaran, "Wavelet thresholding techniques implementation in retinal images for suppressing noises" Materials Today: Proceedings, <https://doi.org/10.1016/j.matpr.2021.12.059>.
2. **V. Venkataramanan** and S. Lakshmi, "A Case Study of Various Wireless Network Simulation Tools", International Journal of Communication Networks and Information Security (IJCNIS), PP.389-396, Vol. 10, No. 2, August 2018.
3. **V. Venkataramanan** and V. Kannan, "Bit Error Rate Analysis of LTE Physical Layer with Different Channel Models," International Journal on Communications Antenna and Propagation (IRECAP), vol. 7, no. 7, p. 641, Dec. 2017
4. C. Sasikiran, **V. Venkataramanan**, "Model-Based Design of LTE Base band processor using Xilinx System Generator in FPGA", ARPN Journal of Engineering and Applied Sciences, pp: 5941-5946, Vol.10, No.14, Aug 15.
5. R. Janani, A. Manikandan, **V. Venkataramanan**, "Software Defined Radio Implementation in 3GPP Systems" ARPN Journal of Engineering and Applied Sciences, pp: 5537-5541, Vol.10, No.13, July 15.

International Conference (SCI)- Publons record - 2

1. J. D. Sanghavi, A. M. Shah, S.S. Rane, and **V. Venkataramanan**, "Smart Traffic Density Management System Using Image Processing," Proceedings of International Conference on Wireless Communication, pp. 301–312, 2018.
2. Aditi Patel, Abhishek Satpute, Mital Pattani, **V Venkataramanan**. "Virtual Piano" Proceedings of International Conference on Wireless Communication, pp: 265-272, Springer, Singapore, 2018.

International Conference - 3

1. C.A Sathiyamoorthy, V Siva Sankaran, **V Venkataramanan**, Mohd Faisal Mohd Sadique, Soumyaparakash Dasmohapatra, "Impact of 5G Technology on Indian Industries" Proceedings of International Conference on Wireless Communication, pp: Springer, Singapore 2021.
2. A Das, **V Venkataramanan**, Study of Performance of an OFDM Transceiver Using SDR Platform, Proceedings of International Conference on Wireless Communication, pp: 363-372, Springer, Singapore 2020.
3. K.S Ramesh and **V. Venkataramanan**, "Fault Coverage and Switching Activity Reduction Using Scan Shift Operation", International Conference on Computer Vision (ICCV-2012) Singapore.

International Book Chapter (Scopus) - 1

1. **V Venkataramanan**, S Lakshmi, W Dalmet, M Khaja, I Kherani, LTE Baseband Receiver Implementation on Simulink, Proceedings of International Conference on Wireless Communication, Lecture Notes on Data Engineering and Communications Technologies, pp: 387-395, Springer, Singapore 2020

International Journal - 6

1. S. Abirami, A. Manikandan, **V. Venkataramanan**. "Interference Cancellation in Multiple input Multiple Output Cognitive Radio Networks", International Journal of Advanced Technology & Engineering Research (IJATER), pp: 28-31, Vol.5, No.6, Nov15.
2. C. Sasikiran, **V. Venkataramanan**, "FPGA Design and Implementation of Different Modulation Schemes Using Xilinx System Generator" International Journal of Applied Engineering Research, pp: 54-59, Vol. 10, No.58, 2015.
3. **Venkataramanan, V.**, Shankar, K., Vinoth, D., Archana, S., "Analysis of Cluster Based Zone Routing Protocol in MANET through Network Simulator" International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-2, Issue-3, February 2013
4. **V. Venkataramanan**, M. Kavitha "Performance Analysis of LTE Physical Layer Based on Release 10" CiiT International Journal of Wireless Communication, Vol 5, No 5, pp: 197-201,2013.
5. A. Manikandan, **V. Venkataramanan**, M. Kavitha, and S. Parvathi, "Performance Analysis of LTE Physical Layer Based on Release 8&9 through Simulink Environment, "International Journal of Advanced Technology & Engineering Research (IJATER), Vol.2, Issue.6, pp: 92- 97, 2012.
6. Ramesh K S, **Venkataramanan V**, Switching Activity Reduction Using Scan Shift Operation, Journal of Computer Applications, vol. 5, Issue EICA2012-4, Feb 2012, pp. 409- 419.

National Journal - 5

1. Aditi Patel, Mital Pattani, Abhishek Satpute, **Venkataramanan. V** "Automated AC Dimmer", Technofocus, pp-27-30, Vol 2, issue 7, Mar 2017. ISSN 2321-0532
2. Rachita Shah, Udit Shah, Rishit Turakhia, **Venkataramanan. V**, "Electronic Voting Machine", Technofocus, pp-8-11, Vol 2, issue 7, Mar 2017. ISSN 2321-0532.
3. Bhaskar Kulkarni, Parth Joshi, Yash Agrawal, and **V Venkataramanan**, "Security Enabled Wireless LED Display for Notice board, Technofocus,, pp-1-4, Vol 1, issue 7, Mar 2016. ISSN 2321-0532, Oct 2016.

4. Megh Shukla, and **V. Venkataramanan** “Road Safety Model”, Technofocus, pp-43-45, Vol 2, issue 6, Mar 2016. ISSN 2321-0532

5. Shashank Rane, Ravi Maurya, Vaibhav Zaveri, Deepak Rayani and **V. Venkataramanan** “Vehicle Movement Detection Using Street Lights”, Technofocus, -26-28, Vol 2, issue 6, Mar 2016.

National Conference - 10

1. Akramsher Khan, Karthik.P, Sudhanthiram.P and **Venkataramanan. V**, “Versatile Self-Navigating Robot base for Agricultural Applications” National Conference on Reliability and Safety Engineering (NCRS-2016), SSN College of Engineering, Dec 2016.

2. Sasikiran.C and **Venkataramanan. V**, “Model Base Design of Modulation Techniques Using Xilinx System Generator in FPGA” National Conference on Microelectronics, Networking and Communication Technologies (NCMNC 2015), SRM University, April 2015.

3. **V. Venkataramanan**, “Performance Analysis of Digital Circuit Design under different FPGA Families” National Conference on Recent Trends in Electronics and Communication (NCRTECE-15), SMK Forma Institute of Technology, March 2015.

4. Sasikiran.C and **Venkataramanan. V**, “An Improved Design of Carry Skip (BYPASS) Adder Using Reversible Logic”, National Conference on Information & Communication Technologies (NCICT 2014), SSN College of Engineering, April 2014.

5. Sharmili. R, and **Venkataramanan. V**, “An Integrated Automatic Test Pattern Generation and Implementation Using Device Under Test (DUT)”, National Conference on Communication system and VLSI Design (NCCSVD-14), PSG College of Technology, Jan 2014.

6. Kavitha. M and **Venkataramanan. V**, “Performance Analysis of LTE Physical Layer Based on Release 10”, National Conference on Advanced computing & Information Technology NACIT-13, Jerusalem Engineering College, May 2013.

7. Kavitha. M and **Venkataramanan. V**, “Performance Analysis of Modulation Technique for MIMO”, National Conference on Information Technology (NCIT – 2012), Pondicherry Engineering College, 2012.

8. K.S Ramesh and **V. Venkataramanan** “Switching activity reduction using Scan Shift operation”, National conference on computational intelligence in Electrical and Electronics Engineering, Sathyabama University, 1st and 2nd March 2012.

9. **V. Venkataramanan**, “FPGA Design and Implementation Channel Estimation and Modulation Technique for MIMO”, National conference on

		<p>Emerging Trends in Micro/Nano Electronic Technology, Saveetha University, 8th April 2011.</p> <p>10. Venkataramanan. V and T. Ramya, “Matlab Implementation of Channel Estimation and Modulation Technique for MIMO”, Proceedings of NCRVIC- 2010, B.S. Abdur Rahman University, March 31, 2010.</p>
Area of Specialization		Wireless, Mobile, VLSI, IoT
Patents/ Books Published	:	<p>Patent:</p> <ol style="list-style-type: none"> 1. An Artificial Intelligence based system for detection of Breast cancer in human body with Application No: 202141007724 A Published <p>Associate Editor in Journal / Book</p> <ol style="list-style-type: none"> 1. Special issue on DJ Strike magazine, with ISBN Number, 2017-2018 2. Special issue on DJ Strike magazine, with ISBN Number, 2018-2019 3. Special issue on DJ Strike magazine, with ISBN Number, 2019-2020 4. Special issue on DJ Strike magazine, with ISBN Number, 2020-2021
Professional Memberships	:	Member of ISTE, IEEE, IETE
Grants fetched		As a Consultant, Proposal awaiting for the approval from the institution AY 21-22
Awards	:	<ol style="list-style-type: none"> 1. Mentored College Level DJS ARYA – CANSAT Team participating in International Competition organized by American Astronautical Society (AAS) received the Certificate of Participation- 2019. The team continuously securing top positions in the worldwide. 2. Received the award for a role of mentor for the College level team on the topic Ecosystem bridging Government organized by Industry and Academia DST & Texas Instruments, India Innovation Challenge Design Contest 2018, May 2018 3. Received the best paper award for the paper titled “FPGA Design and Implementation of LTE Physical layer Modulation Techniques using Xilinx System Generator” organized by Sathyabama Institute of Science and Technology in association with University of Derby RACES-2019, 13th to 15th November 2019. 4. Secured first rank during M.Sc. Electronics and Communication 5 Year Integrated PG degree program-2008
Interaction with Professional Institutions		<p>Guest Lecture</p> <ol style="list-style-type: none"> 1. Conducted Hands on Training “MATLAB for Engineering Applications” and workshop on “How to write the technical paper” as a Resource Person for D.J Sanghvi college in 2017 and 2019 2. Conducted the Workshop on “Simulation Tool: Tinker CAD” as Resource Person at Sathyabama Institute of science and Technology, Chennai, and Builders Engineering College, Kangayam in 2020. <p>Interaction with IITs</p> <ol style="list-style-type: none"> 1. Mentored for NPTEL Course on Digital System Design, Data Structures and Computer Algorithms and Mobile Computing organized by IIT Madras in 2013.

<p>Interaction with Professional Institutions</p>	<p>National / International Conference Chairs</p> <ol style="list-style-type: none"> 1. Session Chair, Jeppiar Institute of science and Technology, 2015 2. Review Committee Member, ICWiCOM conference, 2017, 2019, 2021. 3. Publication Co-Ordinator, Technofocus journal, ISSN: 2321-0532, 2016-2017. <p>Editorial Board Member role:</p> <ol style="list-style-type: none"> 1. International Research Publication House – May 2014 2. Technoarete Transactions on Sensor and its Applications, Technoarete Transactions on Electrical Vehicles and Automotive systems 2021 3. Advances in Mobile Learning Educational Research (AMLER) (ISSN: 2737-5676) <p>Reviewer role</p> <p>SCI/WOS</p> <ol style="list-style-type: none"> 1. The International Journal of Communication Systems (IJCS) – Wiley – March,2020 2. IEEE Journal of Internet of Things (IoT) – IEEE – Jan 2019 3. Elsevier Science Direct Journal – Elsevier Computer Communication - 2013,2022 4. Elsevier Science Direct Journal -Vehicular Communication 2022 5. Elsevier Science Direct Journal - Journal of Networks and Computer Applications 2022 <p>SCOPUS</p> <ol style="list-style-type: none"> 1. Journal of Communications - 2021 2. International Journal of Interactive Mobile Technologies (iJIM)- 2021 3. Indonesian Journal of Electrical Engineering and Computer Science -2020 4. International Journal of Electrical and Electronic Engineering and Telecommunications -2020 5. International Journal of Mobile and Blended Learning (IJMBL) -2021 6. International Journal of Information and Communication Technology Education (IJICTE) -2021
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Subjects Taught	<p><u>UG Level:</u></p> <ol style="list-style-type: none"> 1. Electronic Circuits 2. Digital Principles and System Design 3. Linear Integrated Circuits 4. Electro Magnetic Fields 5. Analog and Digital Communication 6. Microprocessor and Microcontroller 7. Digital Signal Processing 8. VLSI Design 9. High-Speed Networks 10. Mobile Communication Systems 11. Wireless Networks 12. Optical communication 13. Optical Networks 	<p><u>PG Level:</u></p> <ol style="list-style-type: none"> 1. Communication Systems Laboratory 2. VLSI Design Lab II 3. VLSI Signal Processing 4. Low Power VLSI Design 5. VLSI for Wireless Communication
Projects Guided	<p>Collaborative project learning program (2nd year,3rd year UG Students)</p> <ol style="list-style-type: none"> 1. More than 100+ project guided / coordinated in DJ Strike (Innovative product development) Course <p><u>UG Level:6</u></p> <ol style="list-style-type: none"> 1. 2011 Implementation of WIMAX Physical Layer 2. 2011 A Low-cost Extendable Framework for Embedded Smart Car Security System 3. 2012 Performance analysis of MIMO-OFDM system 4. 2012 Digital medical perception 5. 2013 Versatile Self Navigating Robotic Base – Applied for Agricultural Vehicles 6. 2014 Intelligent Human-Machine Interface Using Hand Gestures Recognition 7. 2019 co-simulation of LTE physical layer and its implementation <p><u>PG Level:6</u></p> <ol style="list-style-type: none"> 1. 2012 Switching Activity Reduction Using Shift Operation 2. 2013 Performance Analysis of LTE Physical Layer Based On Releases 8, 9 & 10 3. 2013 Analysis of Cluster Based Zone Routing Protocol in MANET Using in Multicasting through Network Simulator 4. 2015 Testbed Design Flow Analysis of LTE Using In FPGA 5. 2015 Performance Analysis of LTE And LTE A Physical Layer 6. 2015 Implementation of carry skip adder using FPGA 	

<p>Recommended Students for Higher Education (Listed few)</p>	<p><u>Name of the Student</u></p> <ul style="list-style-type: none"> • Shikar Verma • Vineet A Kanetkar • Ravleen Kaur Dehiya • Prachi Gandhi • Rachita Shah • Wallace Dalmet • Anuradha Pandey • Akshay Bogan • Arman Lalani 	<p><u>University/Industry</u></p> <ul style="list-style-type: none"> • University of California, Berkeley • Carnegie Mellon University • University of Southern California • Georgia Tech • Northeastern University • Carnegie Mellon University • Tata Consultancy services • Reliance Jio • Tata Consultancy services
<p>Institute/Department Responsibility handled:</p>	<ol style="list-style-type: none"> 1. Contributed as a member for NBA, NAAC, Autonomous accreditation process 2. Involved in DJSCE Admission process during 2017-2021 3. Mentored College Level DJS ARYA – CANSAT Team participating in International Competition organized by American Astronautical Society (AAS), 2017-2021 4. Coached College Level DJS ANTARIKSH – Rover Team, undertaking international space and robotics event orchestrated by European Rover Challenge, University Rover Challenge and The Indian Rover Challenge, 2019-2022 5. mentor for the College level team on the topic of Ecosystem bridging Government organized by Industry and Academia DST & Texas Instruments, India Innovation Challenge Design Contest 2018, May 2018 6. Innovative product development (DJ STRIKE) course coordinator , 2017-2021 7. Coordinated projects for ECE during 2011 – 2012. 8. Mentored for NPTEL Course on Digital System Design, Data Structures and Computer Algorithms and Mobile Computing organized by IIT Madras. 9. Placement Coordinator during 2014- 2015. 10. Industrial Visit Coordinator, Organizing Major Events in College, Guest Lecture arrangements. 11. Faculty Advisor, Secretary in National Conferences / Symposium held at our institution 	
<p>Pedagogy Development</p>	<ol style="list-style-type: none"> 1. E-Learning video in the subject of IoT-Simulation tool Tinker cad 	